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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re application of: Donald P. Matthews

Atty Docket No.: BRCMP016/BP2009

Application No.: 10/026,109

Examiner: Not yet assigned

Filed: December 20, 2001

Group: 2131

Title: METHODS AND APPARATUS FOR
ACCELERATING ARC4 PROCESSING

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Commissioner for Patents, Washington, DC 20231 on October 23, 2002.

Signed: 

Joyce L. Ferreira

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

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Commissioner for Patents
Washington, DC 20231

OCT 30 2002

Technology Center 2100

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. BRCMP016).

Respectfully submitted,

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Form 1449 (Modified)

Information Disclosure Statement By Applicant

(Use Several Sheets if Necessary)

Atty Docket No. BRCMP016/BP2009

Applicant: Donald P. Matthews

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	5,796,836	08/18/98	Markham			08/29/95
	A2	5,297,206	03/22/94	Orton			10/07/92
	A3	6,111,858	08/29/00	Greaves <i>et al.</i>			02/18/97
	A4	5,943,338	08/24/99	Duclose <i>et al.</i>			08/19/96
	A5	6,216,167	04/10/01	Momirov			06/29/98
	A6	5,936,967	08/10/99	Baldwin <i>et al.</i>			11/27/96
	A7	5,870,474	02/09/99	Wasilewski <i>et al.</i>			12/29/95

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	A8	Analog Devices: "ADSP2141 SafeNetDPS User's Manual, Revision 6", Analog Devices Technical Specifications, March 2000, XP002163401, 87 Pages
	A9	C. Madson, R. Glenn: "RFC 2403- The Use of HMAC-MD5-96 within ESP and AH", IETF Request for Comments, November 1998, XP002163402, Retrieved from Internet on 3/20/01, http://www.faqs.org/rfcs/rfc2403.html , 87 Pages.
	A10	S. Kent, R. Atkinson: "RFC 2406-IP Encapsulating Security Payload (ESP)" IETF Request for Comments, November 1998, XP002163400, Retrieved from the Internet on 3/20/01, http://www.faqs.org/rfcs/rfc2406.html , 5 Pages.
	A11	Keromytis, et al., "Implementing IPsec", Global Telecommunications Conference (Globecom), IEEE, 3 November 1997, pages 1948-1952.
	A12	Pierson, et al., "Context-Agile Encryption for High Speed Communication Networks", Computer Communications Review, Association for Computing Machinery, Vol. 29, No. 1, January 1999, pages 35-49.
	A13	Sholander, et al., "The Effect of Algorithm-Agile Encryption on ATM Quality of Service", Global Telecommunications Conference (Globecom), IEEE, 3 November 1997, pages 470-474.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.